

Attorney Docket: T2147-908627
Application No.: 10/627,976

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Anne KASZYNSKI & Jacques ABILY

Serial No.: 10/627,976

Filed: July 28, 2003

For: Method For Functional Verification Of An
Integrated Circuit Model For Constituting A
Verification Platform, Equipment Emulator
And Verification Platform

: Examiner:
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: Group Art Unit:
:
: Corresponding to:
: French Patent
: Application FR 02 09691

McLean, Virginia

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following amendments and remarks are submitted prior to examination of the above-identified application on the merits.

Amendments to the Specification are presented by the following substitute paragraphs which begin at page 3 that have been marked up to show changes by strikethrough (for deleted matter) and underlining (for added matter), relative to the immediate prior version as set out in 37 CFR 1.121(b), i.e., the literal English translation filed concurrently herewith.

Amendments to the Claims are reflected in the listing of claims beginning on page 13 of this paper.

Amendments to the Abstract are presented on page 20 of this paper.

Amendments to the Drawings are presented on page 21 of this paper, and replacement annotated drawings are submitted for approval.